



राष्ट्रीय प्रौद्योगिकी संस्थान नागालैंड  
NATIONAL INSTITUTE OF TECHNOLOGY NAGALAND  
Chumukedima, Dimapur  
Nagaland - 797 103

Advt.No: NIT-N/SMDP/Recruit/01/2019

Dated 12.12.2019

**Walk-in-Interview**

National Institute of Technology (NIT) Nagaland invites applicants for **Walk-In-Interview** for the recruitment of Project Staff (Lab Engineer and Project Assistant) on contractual basis under “Special Manpower Development Project – Chips to System Design (SMDP-C2SD)”.

Sl.No	Post	Vacancy	Desired Qualification	Consolidated Salary
1	Lab Engineer	01	M.E./ M.Tech./ ECE / Instrumentation / Embedded Systems / VLSI <b>Desirable:</b> Having knowledge in VLSI CAD tools preferably experience in working with Xilinx 7 series FPGA and PCB Design	Rs.34,000/- p.m. (Consolidated)
2	Project Assistant	01	B.E./B.Tech./ ECE / Instrumentation / Embedded Systems / VLSI <b>Desirable:</b> Having knowledge in VLSI CAD tools preferably experience in working with Xilinx 7 series FPGA and PCB Design	Rs.20,000/- p.m. (Consolidated)

**Date and Time:** 10-01-2020, 10:00 a.m. onwards

**Venue:** Administrative Block, NIT Nagaland, Chumukedima, Dimapur - 797 103, Nagaland

The appointment is purely on temporary basis for a period of one year or co-terminus of the project. However, the contract may be extended or terminated at any point of time further depending on the requirement and qualitative performance. One set of photocopy of all certificates and one passport size self- attested photo are to be produced with the application at the time of Interview. Original Certificates shall be shown at the time of Interview for verification.

No TA / DA will be given for appearing the Interview.

**(DIRECTOR)**