

NATIONAL INSTITUTE OF TECHNOLOGY NAGALAND
ACADEMIC YEAR 2020-21- ODD SEMESTER (OCT'20 – FEB' 21)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

M. Tech (Communication Engg.) - Semester I
Class Advisor: Dr. Madhusudan Singh

Day	9:20 – 10:10	10:20- 11:10	11:20- 12:10	12:20 – 1:10	1:10 – 2:00	2:10 – 3:00	3:10 – 4:00
Mon	MA501	CM501	CM502	Lunch	CM503	CM504	
Tue	CM501	CM502	CM913		CM503		
Wed	CM913	CM501	CM502		CM504		
Thu	CM913	CM501	CM504		MA501		
Fri		CM502	MA501		CM503		
Sat	CM504	MA501	CM503		CM913		

Sl. No.	Course Code	Course Title	Credits	Course Co-Ordinator
1.	MA501	Applied Mathematics	3	Dr. P. P. Mishra + Dr. Anirban
2.	CM501	Advanced Digital Communication Technique	3	Dr. Debadatta Pati
3.	CM502	Information and Coding theory	3	Mr. Monish Kumar Chaudhury
4.	CM503	Communication Networks and Protocols	3	Ms. Thoibileima
5.	CM504	Wireless Communication	3	Ms. Ngasepam Monica
6.	CM913	Elective – I	3	Dr. P. Chinnamuthu
7.	CM506	Communication System Simulation Laboratory	2	-----

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

M. Tech (VLSI Systems) - Semester I

Class Coordinator – Dr. P. Roji Chanu

Day	9:20 – 10:10	10:20- 11:10	11:20- 12:10	12:20 – 1:10	1:10 – 2:00	2:10 – 3:00	3:10 – 4:00
Mon	VL501		VL502	Lunch	MA504	VL8YY	
Tue	VL501	VL502	VL504		MA504		
Wed	VL504	VL501	VL502		VL8YY		
Thu	VL504	VL8XX	VL8YY		VL8XX		
Fri		VL502	VL8XX		MA504		
Sat	VL8YY	VL8XX	MA504		VL504		

Sl. No.	Course Code	Course Title	Credits	Course Co-Ordinator
1.	MA504	Advanced Computational Mathematics	3	Mr. Chipem
2.	VL8XX	System on Chip	3	Ms. Jayshree
3.	VL501	Advanced VLSI Design	3	Dr. Jay Chandra Dhar
4.	VL502	Advanced Digital Communication Technique	3	Dr. Debadatta Pati
5.	VL504	Verilog based design of digital systems	3	Dr. Naorem Khelchand Singh
6.	VL8YY	Compact Modelling Devices of Digital Systems	3	Mr. Palepu Ashok
7.	VL516	VLSI Design Laboratory	2	Dr. Naorem Khelchand Singh